Line 4, cancel ", characterised in that it" and insert in lieu thereof --. The protocol processor--.

Line 10, change "the said" to --an--.

IN THE SPECIFICATION:

Page 1, line 9, change the comma "," to a colon --:--.

Page 1, line 11, change "digital" to --digital signal--.

Page 1, line 11, change "(DSP)" to --(DSP) and--

Page 1, line 16, change "processor" to --processor and--.

Page 1, line 16, change "are the" to --may be identified as--.

Page 1, line 30, change "digital" to --digital signal--.

Page 2, line 35, cancel "the".

Page 2, line 36, change "a routine" to --routines--.

Page 2, line 36, cancel "(sic)".

Page 2, line 37, change "a", second occurrence, to --a data processor system including a main processor and a protocol processor with a--.

Page 2, line 38, change "coming into" to --being interconnected between the main processor and--.

Page 4, line 22, change "consists of" to --comprises--.

Page 4, line 22, change "digital" to --digital signal--.

Page 5, line 30, change "consists of" to --comprises--.

Page 5, line 34, change ", the" to --. The--.

Page 5, line 35, change "synchronising" to --synchronizing--.

Page 6, lines 3, 8 and 25, change "synchrononising" to --synchronizing--.

Page 7, line 18, cancel "the" (first occurrence).

Page 7, line 18, change "this figure" to --Figure 9--.

Page 7, line 36, change "synchronisation" to --synchronization--.

Page 7, line 37, change "consists of" to --comprising--.

Page 7, line 38, change "multiplexors" to --multiplexers--.

Page 8, line 12, cancel "a".

Page 8, line 13, change "bus" to --buses--.

Page 10, lines 3, 31, and 35, change "multiplexor" to --multiplexer--.

Page 10, line 29, change "if" to --is--.

Page 12, line 22, change "synchronising" to --synchronizing--.

Page 12, line 34, change "in" to --in a--.

IN THE CLAIMS:

Cancel Claims 1-5.

Please add the following claims:

6. An apparatus, comprising:

a first processor comprising a core, a program memory and a local memory;

a second processor comprising a core, a program memory and a local memory;

a synchronizing circuit for coupling said core of said first processor to said core of said second processor; and

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